AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 6, line 7, with the following rewritten paragraph:

The above and other advantages of the present invention will become more apparent by describing preferred embodiments thereof in detail with reference to the accompanying drawings in which:

Fig. 1A is a layout view of a contact of a semiconductor device according to an embodiment of the present invention;

Fig. 1B is a sectional view of the contact shown in Fig. 1A taken along the line IAB-IAB';

Fig. 1C is a sectional view of the contact structure shown in Fig. 1A and 1B in an intermediate step in a manufacturing method thereof according to an embodiment of the present invention;

Fig. 2 is a schematic diagram of an LCD according to an embodiment of the present invention;

Fig. 3 is a layout view of an exemplary TFT array panel for an LCD according to an embodiment of the present invention;

Fig. 4 is a sectional view of the TFT array panel shown in Fig. 3 taken along the line IV-IV';

Figs. 5A, 6A, 7A and 8A are layout views of the TFT array panel shown in Figs. 3 and 4 in intermediate steps of a manufacturing method thereof according to an embodiment of the present invention;

Figs. 5B, 6B, 7B and 8B are sectional views of the TFT array panel shown in Figs. 5A, 6A, 7A and 8A taken along the lines VB-VB', VIB-VIB', VIIB-VIIB' and VIIIB-VIIIB', respectively;

Fig. 9 is a layout view of an exemplary TFT array panel for an LCD according to another embodiment of the present invention;

Figs. 10 and 11 are sectional views of the TFT array panel shown in Fig. 9 taken along the lines X-X' and XI-XI', respectively;

Fig. 12A is a layout view of a TFT array panel shown in Figs. 9-11 in the first step of a manufacturing method thereof according to an embodiment of the present invention;

Figs. 12B and 12C are sectional views of the TFT array panel shown in Fig. 12A taken along the lines XIIB-XIIB' and XIIC-XIIC', respectively;

Figs. 13A and 13B are sectional views of the TFT array panel shown in Fig. 12A taken along the lines XIIB-XIIB' and XIIC-XIIC', respectively, and illustrate the step following the step shown in Figs. 12B and 12C;

Fig. 14A is a layout view of the TFT array panel in the step following the step shown in Figs. 13A and 13B;

Figs. 14B and 14C are sectional views of the TFT array panel shown in Fig. 14A taken along the lines XIVB-XIVB' and XIVC-XIVC', respectively;

Figs. 15A, 16A and 17A, and Figs. 15B, 16B and 16B are respective sectional views of the TFT array panel shown in Fig. 14A taken along the lines XIVB-XIVB' and XIVC-XIVC', respectively, and illustrate the steps following the step shown in Figs. 14B and 14C:

Fig. 18A is a layout view of a TFT array panel in the step following the step shown in Figs. 17A and 17B;

Figs. 18B and 18C are sectional views of the TFT array panel shown in Fig. 18A taken along the lines XVIIIB-XVIIIB' and XVIIIC-XVIIIC', respectively;

Fig. 19 is a layout view of an exemplary TFT array panel for an LCD according to another embodiment of the present invention;

Fig. 20 is a sectional view of the TFT array panel shown in Fig. 19 taken along the line XX-XX';

Figs. 21A and 22A are layout views of the TFT array panel shown in Figs. 19 and 20 in intermediate steps of a manufacturing method thereof according to an embodiment of the present invention;

Figs. 21B and 22B are sectional views of the TFT array panel shown in Figs. 21A and 22A taken along the lines XXIB- XXIB' and XXIIB-XXIIB', respectively;

Fig. 23 is a sectional view of a TFT array panel according to another embodiment of the present invention;

Figs. 24A and 24B are sectional views of the TFT array panel shown in Fig. 23 in intermediate steps of a manufacturing method thereof according to an embodiment of the present invention;

Figs. 25A and 25B are sectional views of the TFT array panel shown in Fig. 23 in intermediate steps of a manufacturing method thereof according to another embodiment of the present invention;

Fig. 26 is a layout view of an exemplary TFT array panel for an LCD according to another embodiment of the present invention;

Figs. 27 and 28 are sectional views of the TFT array panel shown in Fig. 26 taken along the lines XXVII-XXVII' and XXVIII-XXVIII', respectively; and

Fig. 29 is a sectional view of a TFT array panel according to another embodiment of the present invention.

Please replace the paragraph beginning at page 8, line 29, with the following rewritten paragraph:

Fig. 1A is a layout view of a contact of a semiconductor device according to an embodiment of the present invention, Fig. 1B is a sectional view of the contact shown in Fig. 1A taken along the line IAB-IAB', and Fig. 1C is a sectional view of the contact structure shown in Fig. 1A and 1B in an intermediate step in a manufacturing method thereof according to an embodiment of the present invention.